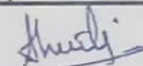
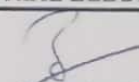


Mahatma Education Society's  
**Pillai HOC Polytechnic.Rasayani**  
**Time Table : Summer 2020 Theory Examination**  
 ( As per guidelines givenby MSBTE in Circular dated 15th sep 2020)  
**Department :Electronics Engineering**

Day	Date of Examination	Session	Time	Scheme	Paper Code	Candidate Count	Name of Subject
1	25/09/2020	Morning	11:00 am to 12:00 pm	3G	17319	3	ELECTRONICS DEVICES AND CIRCUITS
2	26/09/2020	Morning	11:00 am to 12:00 pm	4G	17445	2	LINEAR INTEGRATED CIRCUITS
3	27/09/2020	Morning	11:00 am to 12:00 pm	4G	17444	1	POWER ELECTRONICS
4	28/09/2020	Morning	10:00 am to 11:00 am	3G	17301	6	APPLIED MATHEMATICS
5	29/09/2020	Morning	11:00 am to 12:00 pm	5G	17537	3	AUDIO VIDEO ENGINEERING
6	30/09/2020	Morning	11:00 am to 12:00 pm	3G	17318	2	ELECTRICAL ENGINEERING
7	01/10/2020	Morning	11:00 am to 12:00 pm	5G	17535	4	DIGITAL COMMUNICATION
8	03/10/2020	Morning	11:00 am to 12:00 pm	5G	17536	4	CONTROL SYSTEM & PLC
				5E	12189	1	INDUSTRIAL ELECTRONICS

  
HOD

  
Principal