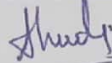



Mahatma Education Society's
Pillai HOC Polytechnic.Rasayani
Time Table : Summer 2020 Theory Examination
 (As per guidelines givenby MSBTE in Circular dated 15th sep 2020)
Department :Electronics & Telecommunication Engineering

Day	Date of Examination	Session	Time	Scheme	Paper Code	Candidate Count	Name of Subject
1	25/09/2020	Morning	11:00 am to 12:00 pm	3G	17320	1	PRINCIPLE OF DIGITAL TECHNIQUES
				3G	17319	1	ELECTRONICS DEVICES AND CIRCUITS
				3G	17317	1	ELECTRONIC INSTRUMENTS & MEASUREMENT
2	26/09/2020	Morning	11:00 am to 12:00 pm	5G	17533	2	COMPUTER HARDWARE & NETWORKING
3	27/09/2020	Morning	11:00 am to 12:00 pm	5G	17534	2	MICROCONTROLLER
				4G	17444	2	POWER ELECTRONICS
				4I	22427	1	BASIC POWER ELECTRONICS
4	28/09/2020	Morning	10:00 am to 11:00 am	3G	17301	11	APPLIED MATHEMATICS
5	29/09/2020	Morning	11:00 am to 12:00 pm	5G	17537	2	AUDIO VIDEO ENGINEERING
6	30/09/2020	Morning	11:00 am to 12:00 pm	3G	17318	3	ELECTRICAL ENGINEERING
				4I	22425	1	CONSUMER ELECTRONICS
7	01/10/2020	Morning	11:00 am to 12:00 pm	5G	17535	4	DIGITAL COMMUNICATION
				4I	22428	1	DIGITAL COMMUNICATION SYSTEMS
8	03/10/2020	Morning	11:00 am to 12:00 pm	5G	17536	4	CONTROL SYSTEM & PLC


HOD


Principal