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MAHARASHT (Autonomous) (ISO/IEC - 2700

WINTER-19 EXAMINATION

Subject Name: Basic Power Electronics

Subject Code:

22427

#### Model Answer

**Important Instructions to examiners:** 

- 1) The answers should be examined by key words and not as word-to-word as given in themodel answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may tryto assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given moreImportance (Not applicable for subject English and Communication Skills.
- 4) While assessing figures, examiner may give credit for principal components indicated in thefigure. The figures drawn by candidate and model answer may vary. The examiner may give credit for anyequivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constantvalues may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.

Q. No.	Sub Q. N.	Answers	Marking Scheme
1	(A)	Attempt any FIVE of the following:	10- Total Marks
	(a)	Draw labeled symbol of SBS and PUT.	2M
	Ans:	Symbol: SBS PUT	1M each
	(b)	State two applications of GTO.	2M
	Ans:	<ul> <li>Applications of GTO: (Any two)</li> <li>Variable speed motor drives.</li> </ul>	1M each

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#### WINTER-19 EXAMINATION

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Subject Code: 22427

## Model Answer

	<ul> <li>Traction.</li> <li>DC drives or DC choppers.</li> <li>AC drives.</li> <li>AC stabilizing power supplies.</li> <li>Induction heater.</li> <li>Field oriented control scheme used in rolling mills, robotics and machine tools.</li> </ul> (Note: Any other application may also be considered).	
(c)	List the types of turn-off methods of SCR.	2M
Ans:	<ul> <li>Natural Commutation</li> <li>Forced Commutation         <ol> <li>Class A: Self commutated by a resonating load.</li> <li>Class B: Self commutated by an LC circuit.</li> <li>Class C: C or L-C switched by another load carrying SCR.</li> <li>Class D: C or L-C switched by an auxiliary SCR.</li> <li>Class E: An external pulse source for commutation.</li> <li>Class F: AC line commutation.</li> </ol> </li> <li>Draw circuit diagram of single phase center - tapped full wave controlled rectifier with R-</li> </ul>	2M
	load.	
Ans:	$T_1$ $A$ $e = E_m \sin \omega t$ $R$ $A$ $R$	2M

**Subject Name: Basic Power Electronics** 

Subject Code: 22427

## Model Answer

	Define:	1M
	A converter is a solid state device/circuit for converting electrical energy. It may be converting AC to or from DC, or the voltage or frequency, or some combination of these.	
	Types:	
	1. Choppers.	
	2. Inverters.	1M
f)	Define term Inverter.	2M
Ans:	An inverter is a solid state device that converts direct current (DC) to alternating current (AC).	2M
g)	Draw circuit diagram of light dimmer using DIAC- TRIAC.	2M
Ans:	AC SUPPLY	2M

Q. No.	Sub Q. N.	Answers	Marking Scheme
2		Attempt any THREE of the following:	12- Total Marks
	a)	Draw V-I characteristics of SCR. Define holding current and latching current.	4M

**Subject Name: Basic Power Electronics** 

Subject Code: 22427

# Model Answer

Ans:	V-I characteristics of SCR:	2M - char
	<ul> <li>For ward conduction         <ul> <li>(on state)</li> <li>(on state)</li> <li>(urrent</li> <li>(lg3&gt;lg2&gt;lg1</li> <li>(lg3)</li> <li>(lg2)</li> <li>(lg3)</li> <li>(lg2)</li> <li>(lg3)</li> <li>(lg2)</li> <li(lg2)< li=""></li(lg2)<></ul></li></ul>	1M - latch curre 1M -
	<ul> <li>Holding current: It is the minimum anode to cathode current required to hold the SCR in the ON state. When the anode current goes below the holding current, the device will go to OFF state.</li> </ul>	holdi curre
b)	Describe the working of battery charger using SCR.	4M
Ans:	<ul> <li>Working : <ul> <li>The figure shows the battery charger circuit using SCR.</li> <li>A 12V discharged battery is connected in the circuit and switch SW is closed. The single-phase 230V supply is stepped down to (15-0-15) V by a centre-tapped transformer.</li> <li>The diodes D1 and D2 forms full wave rectifier and pulsating DC supply appears across terminals A and B.</li> <li>When SCR is off, its cathode is held at the potential of discharged battery.</li> <li>During each positive half-cycle, as the diode D3 is forward biased, gate pulse is provided</li> </ul> </li> </ul>	2M – circui diagra

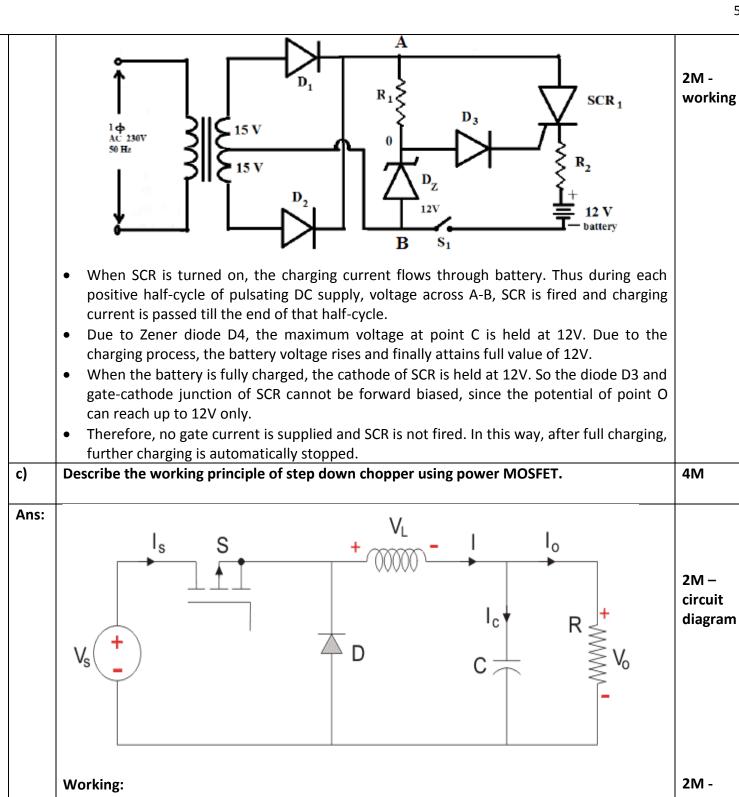
**Subject Name: Basic Power Electronics** 

WINTER-19 EXAMINATION

**Model Answer** 

Subject Code:





working When the MOSFET is turned ON, Vs directly appears across the load as shown in figure.

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WINTER-19 EXAMINATION

**Subject Name: Basic Power Electronics** 

Subject Code: 22427

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	So VO = VS. • When the MOSEET is turned off. Vs is disconnected from the load. So output voltage VO.	
	<ul> <li>When the MOSFET is turned off, Vs is disconnected from the load. So output voltage VO = 0.</li> </ul>	
	Disruption of voltage causes the inductor to induce a voltage.  The free healthead inductor to induce a voltage.	
	The freewheeling diode provides path for load current when MOSFET is OFF.	
	• Thus the energy stored in the inductor during the ON period is dissipated in the load	
	resistance, and helps to maintain a constant current through the load.	
d)	With neat sketch describe the operation of pulse transformer used in triggering circuits of SCR.	4M
Ans:	4	
		2M –
		circui
		diagra
	+	
	F. Trigger pulse	
	$\mathcal{L}_i$ generator $\mathcal{O} = \mathcal{O} = $	
	• K	
	Explanation:	
	• Pulse transformers are often used to couple a trigger pulse generator to a thyristor in	
	order to obtain electrical isolation between the two circuits.	2M -
	• The transformers commonly used for thyristor control are either 1:1 two winding or 1:1:1	Expla
	<ul> <li>three winding types.</li> <li>Figure shows a complete output circuit to fire a thyristor correctly.</li> </ul>	ion
	<ul> <li>The series resistor R either reduces the SCR holding current or balances gate current in a</li> </ul>	
	three winding transformer connected to two SCRs.	
	• The series diode D prevents reverse gate current in the case of ringing or reversal of the	
	pulse transformer output voltage.	
	• The diodes also reduce holding current of the SCR. In some cases where high noise levels	
	are present it may be necessary to load the secondary of the transformer with a resistor	
	to prevent false triggering.	1

**Subject Name: Basic Power Electronics** 

Subject Code: 22427

# Model Answer

Q. No.	Sub Q. N.	Answers	Marking Scheme
3		Attempt any THREE of the following :	12- Total Marks
	a)	With the help of circuit diagram and waveforms explain the working of single phase half wave controlled rectifier with R-load.	4M
	Ans:	Circuit diagram: $ \begin{array}{c}                                     $	Circuit:2M explanatio n:1M waveform s:1M
		Waveforms:	

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**Subject Name: Basic Power Electronics** 

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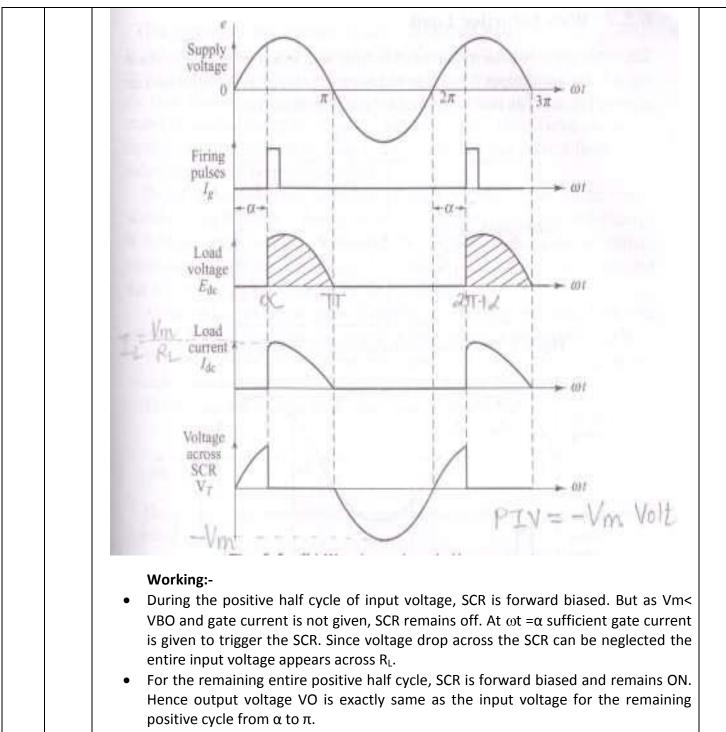
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(Autonomous) (ISO/IEC - 2700

WINTER-19 EXAMINATION

Subject Code:

22427



• During the negative half cycle, at  $\omega t = \pi$  SCR is reverse biased and remains off. It will continue remain off in the next positive half cycle until triggered by gate current at  $2\pi + \alpha$ .



**Subject Name: Basic Power Electronics** 

Subject Code:

22427

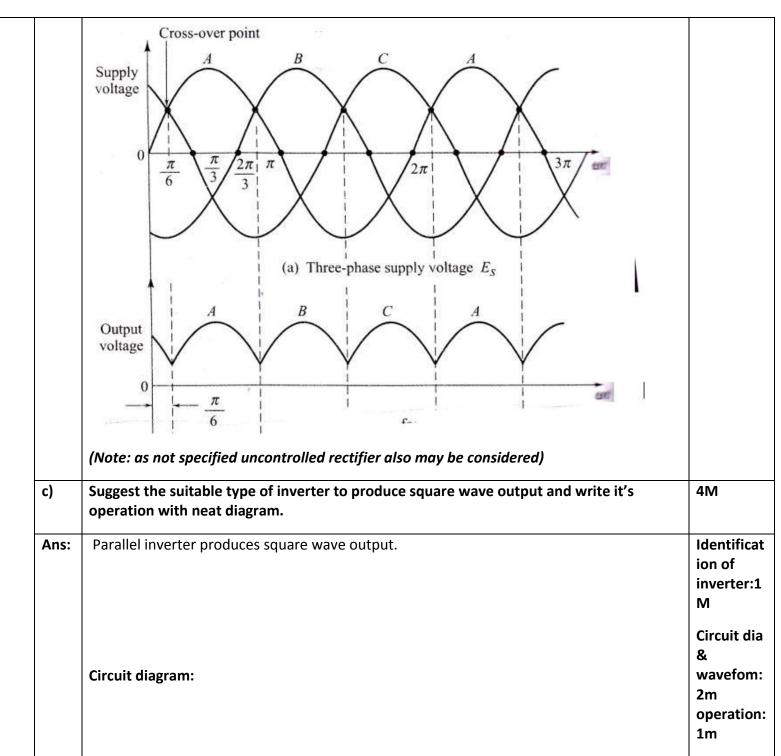
b)	Draw circuit diagram and input and output voltage waveforms of 3- $\Phi$ half wave rectifier with resistive load.	4M
Ans:	Circuit diagram:-	Circuit: 2M wave forms:-2N

**Subject Name: Basic Power Electronics** 

WINTER-19 EXAMINATION

Subject Code:

22427

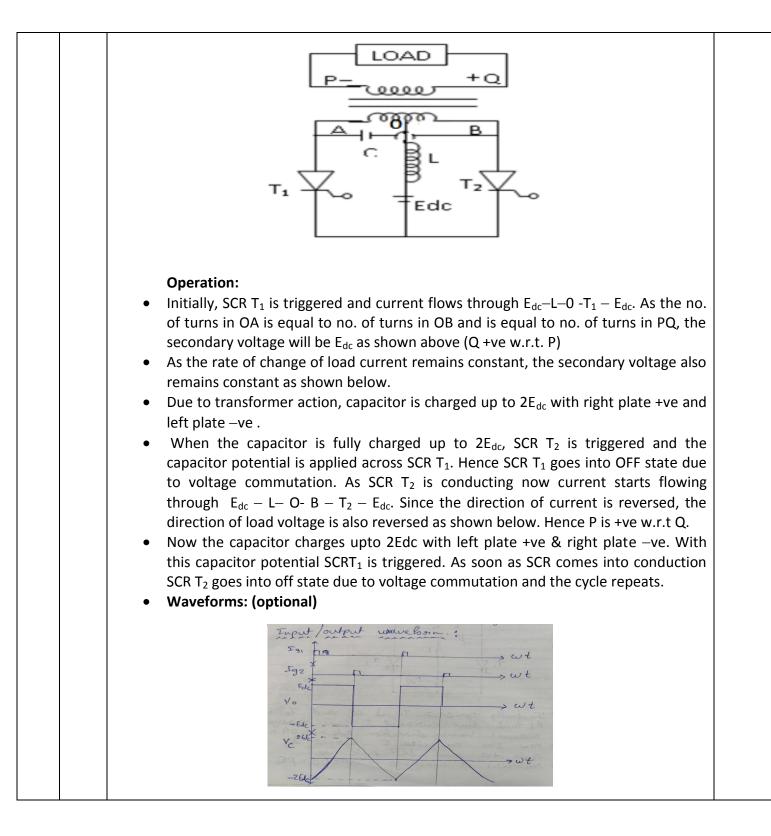


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Subject Code:

22427

#### Model Answer



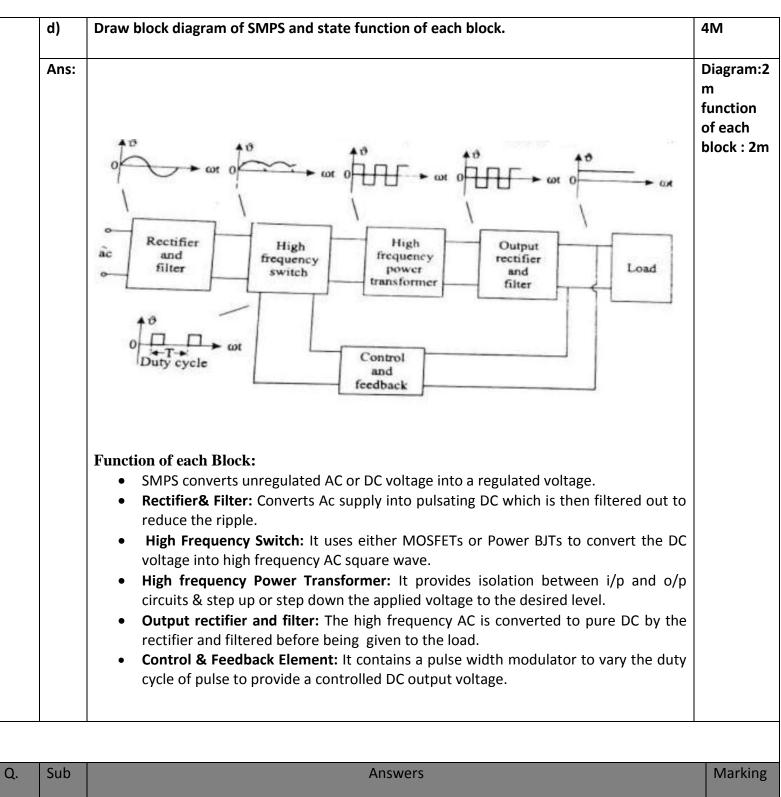


**Subject Name: Basic Power Electronics** 

Subject Code:

22427

#### Model Answer



**Subject Name: Basic Power Electronics** 

Subject Code:

22427

No.	Q. N.		Scheme
4		Attempt any THREE of the following :	12- Total Marks
	(a)	Describe the working of class C commutation with neat diagram and waveforms.	4M
	Ans:	Circuit Diagram:	Circuit diagram : 2M Working : 2M
		<ul> <li>Working:-</li> <li>At first, when the SCR1 is triggered load current flows IL starts flowing through (Vdc+, RL, SCR1, Vdc-).</li> <li>At the same time, capacitor 'C' will charge through Vdc+, R, C, SCR1, Vdc- with right side plate positive.</li> <li>When it is fully charged to Vs charging current becomes zero.</li> <li>To turn off SCR1, SCR2 is triggered.</li> </ul>	

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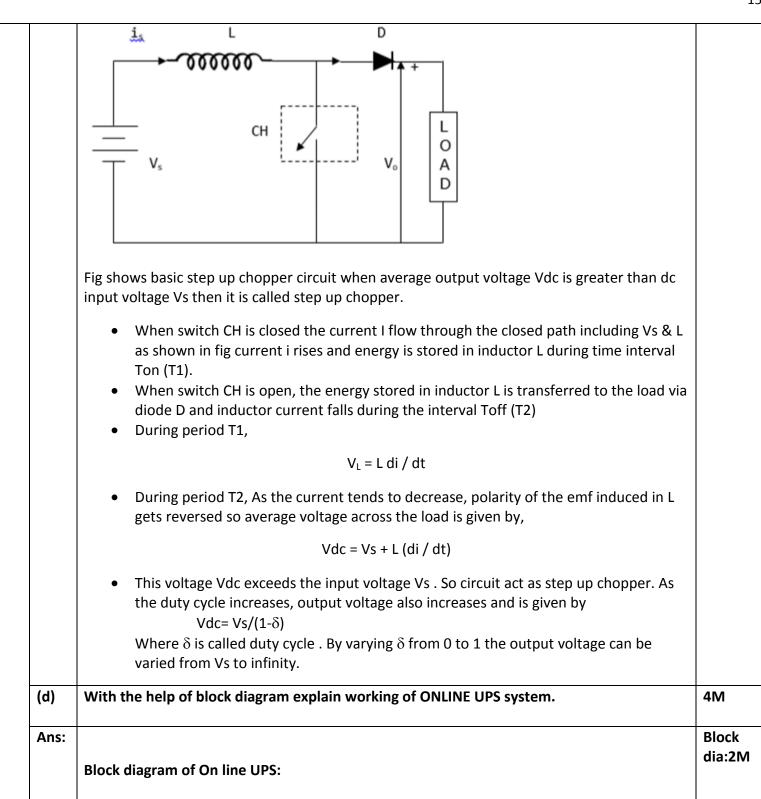
Subject Code: 22427

	• When SCR2 is turned ON the reverse voltage across 'C' is applied across SCR1, turning	
	it OFF.	
	<ul> <li>Now capacitor will start charging through Vdc+, RL, C, SCR2, Vdc- with left side plate positive.</li> </ul>	
	• Similarly, as SCR1 is turned ON the reverse voltage across 'C' is applied across SCR2, turning SCR2 OFF.	
	(Note: Waveform is optional).	
(b)	A single phase fully controlled rectifier supplied with voltage V=100 sin 314t, $\alpha$ = 30° and load resistance is 50 $\Omega$ . Find average output DC voltage and load current.	4M
Ans:	Given:	Formu
	V= 100 sin 314 t	:1/2m each,
	$\alpha = 30^{\circ}$	Vdc:2r Idc : 1ı
	$R_L = 50 \Omega$	
	Required:	
	Vdc = ?	
	I <sub>L</sub> = ?	
	Solution:	
	Average output voltage = $\frac{Vm}{\pi}$ (1 + cos $\alpha$ )	
	$=\frac{100}{\pi}(1+\cos 30)$	
	= 31,8309* 1.866 = 59.396 V	
	Load current $I_{L} = \frac{Vdc}{RL} = \frac{59.396}{50} = 1.188 \text{ A}$	
(c)	Describe the working of step up chopper with neat circuit diagram.	4M
Ans:	Circuit diagram:	Circuit
		m workir
		2m

**Subject Name: Basic Power Electronics** 

Subject Code:

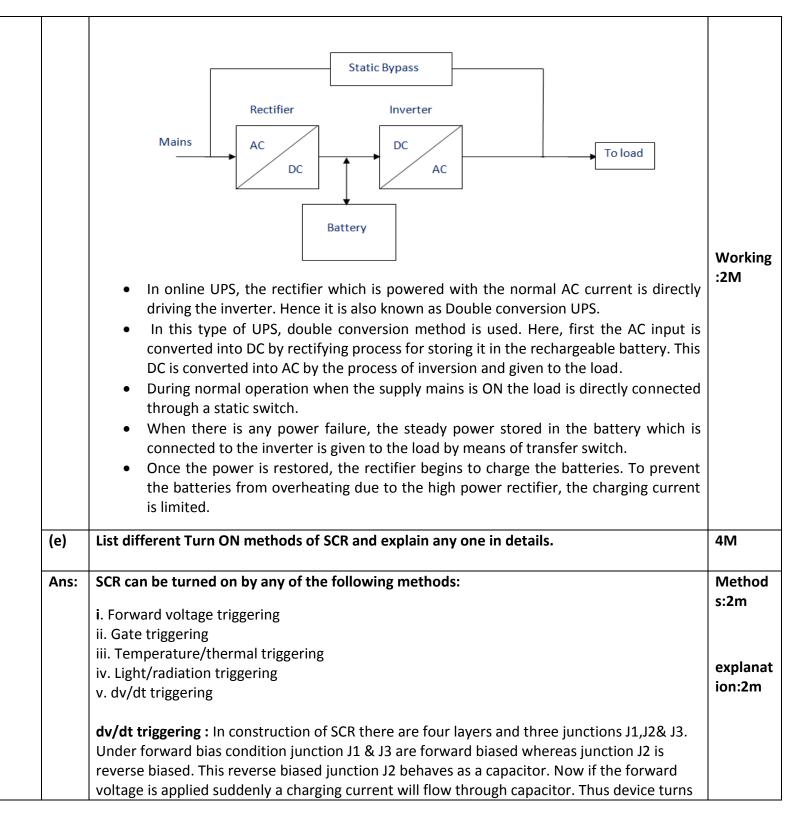
22427



**Subject Name: Basic Power Electronics** 

Subject Code:

22427



**Subject Name: Basic Power Electronics** 

Subject Code: 22427

# Model Answer

	on.	
	If V = voltage applied across the device	
	C <sub>J</sub> = junction capacitance	
	Then the instantaneous current is due to suddenly applied voltage is	
	$I_{\rm C} = C_{\rm J}  {\rm d} {\rm v} / {\rm d} {\rm t}$	
	If dv/dt is large, the device may turn-on or trigger on, even when the voltage across the device is small.	

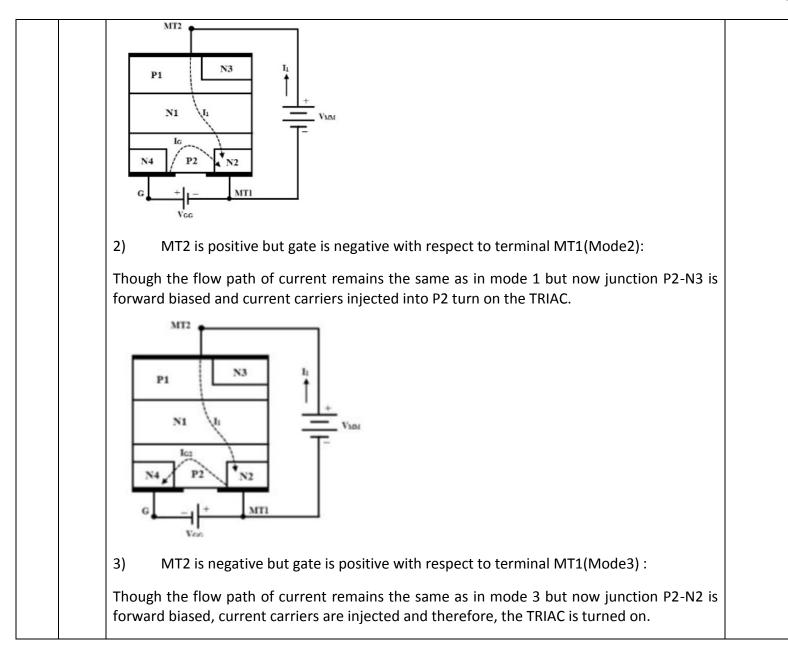
Q. No.	Sub Q. N.	Answers	Marking Scheme
5.		Attempt any TWO of the following:	12- Total Marks
	a)	Suggest a suitable power device having 1 <sup>st</sup> and 3 <sup>rd</sup> quadrant symmetrical characteristics and describe its operation with modes.	6M
	Ans:	<ul> <li>Power device having 1<sup>st</sup> and 3<sup>rd</sup> quadrant symmetrical characteristics is TRIAC</li> <li>There are four different operating modes of TRIAC:</li> <li>MT2 and gate are positive with respect to terminal MT1(Mode1) :</li> </ul>	2 Marks
		Here terminal MT2 is positive with respect to terminal MT1 current flows through path P1-N1-P2-N2.The two junctions P1-N1 and P2-N2 are forward biased whereas junction N1-P2 is blocked. The TRIAC is now said to be positively biased. A positive gate with respect to terminal MT1 forward biases the junction P2-N2 and the breakdown occurs as in a normal SCR.	(Each mode 1 Mk)

**Subject Name: Basic Power Electronics** 

Subject Code:

22427

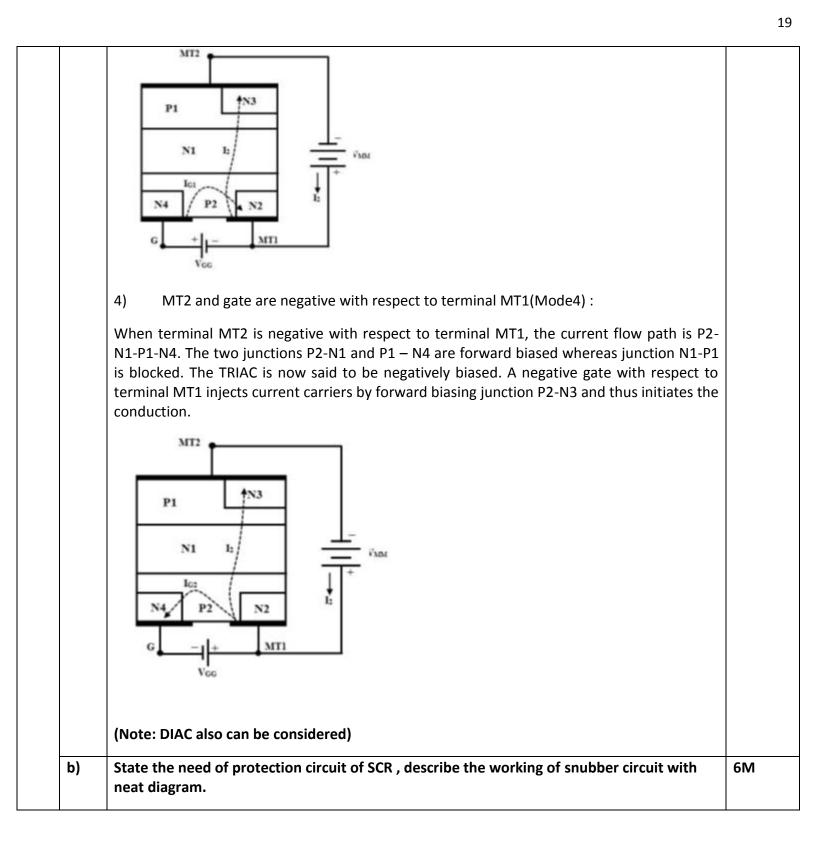
### Model Answer



**Subject Name: Basic Power Electronics** 



22427



**Subject Name: Basic Power Electronics** 

Subject Code: 22427

## Model Answer

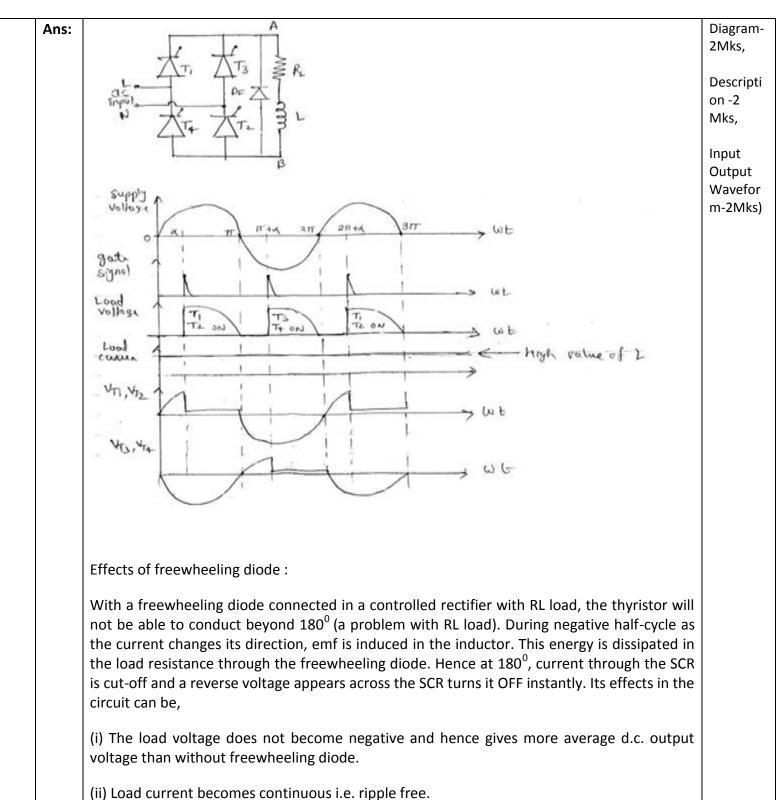
Ans:	Need of protection circuit:	NEED 2 Mks
	In most Power electronic circuits, protection is necessary against the effects of excessive rate of rise of forward voltage ( dv/dt) across the devices, which can otherwise cause unintended break over, leading to malfunction of the circuit and possible failure of the devices. i(t) Thyristor current $i(t)$ $I$	Diagra 2Mks Descri on -2 Mks
	$\frac{1}{1 + E} = \frac{V(t)}{t} + \frac{1}{t} + \frac{1}{t}$	
	capacitor builds up at slow rate such that dv/dt across the capacitor is too small to turn ON the SCR. Therefore, the dv/dt across the SCR and the capacitor is less than the maximum dv/dt rating of the SCR.	
	When Thristor T1 turns on, the snubber capacitor C discharges in to the SCR. But resistor R limits the discharge current and prevents excessive di/dt at turn on. Thus, the presence of resistor impairs the forward dv/dt limiting performance of the snubber. This also consist an inductance in series with the SCR to prevent a high di/dt.	
c)	Draw full-wave controlled rectifier with R-L load with Free wheeling diode. Explain the effect of free wheeling diode on the circuit with output voltage waveforms.	6M

Subject Code:

22427

#### Subject Name: Basic Power Electronics





**Subject Name: Basic Power Electronics** 

Subject Code:

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(iii) It prevents reversal of load voltage.		
(iv) Input power factor is improved.		

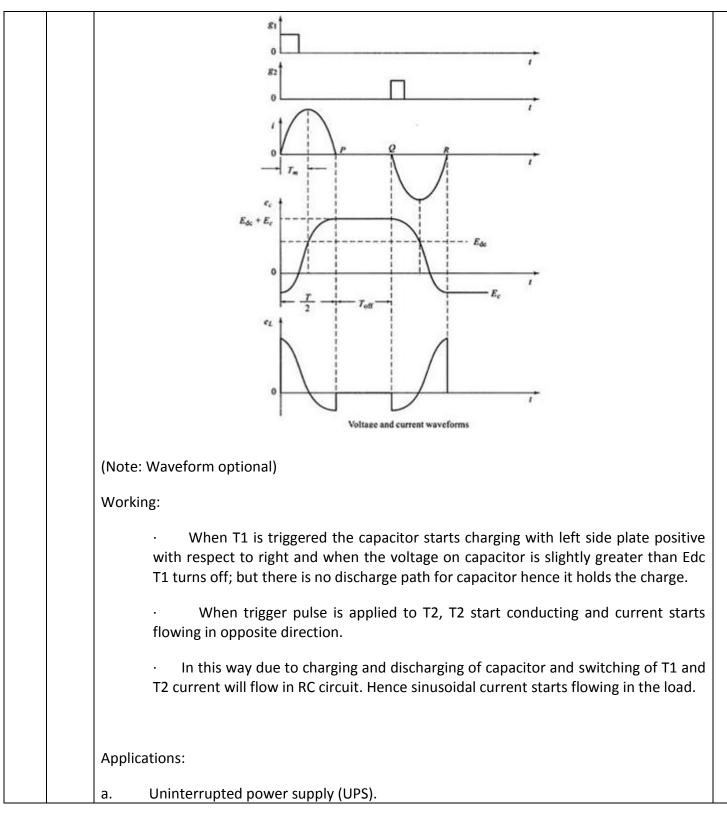
Q. No.	Sub Q. N.	Answers	Marking Scheme
6.		Attempt any TWO of the following :	12- Total Marks
	a)	Describe the working of series Inverter with neat diagram and state its two applications.	6M
	Ans:	$F_{dc}$ $Circuit diagram$	(Diagra m-2 Mks , Working -2Mks, 2 Applicati ons-2 Mks)

**Subject Name: Basic Power Electronics** 

Subject Code:

22427

**Model Answer** 



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WINTER-19 EXAMINATION

Subject Code: 22427

# Model Answer

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	<ul> <li>b. AC motor speed controller.</li> <li>c. Centrifugal fans and pumps.</li> <li>d. Conveyors.</li> <li>e. Induction heating.</li> </ul>	
	f. Aircraft power supply	
	g. High voltage DC transmission lines	
	h. Battery vehicle drives.	
	i. Regulated voltage and frequency power supplies	
b)	Describe the working of LASCR with it's constructional diagram and state its two industrial applications.	6M
Ans:	LIGHT GATE LEAD HERMETIC VELDED WELDED SENSITIVE AREA Basic Construction	(Constru ction Diagram -2 Mks , Working -2Mks, 2 Applicati ons-2 Mks)
	i) LASCR is turned on by direct radiation of light on the silicon wafer.	
	The second territed on by direct radiation of light of the sincon water.	

**Subject Name: Basic Power Electronics** 

Subject Code: 22427

		V-I characte ristics -
Ans:	Circuit diagram:	Circuit Diagram -2Mks ,
c)	With help of circuit diagram and V-I characteristics, explain working principle of UJT and state it's two applications.	6M
	<ul><li>7) Used in high voltage dc transmission (HVDC)</li><li>8) Static reactive power or volt ampere reactive (VAR) compensation.</li></ul>	
	6) Used in computer	
	5) Motor speed control	
	4) Relays	
	3) Photoelectric control	
	2) Triggering circuits	
	1) light coupling	
	Applications of LASCR	
	v) LASCR will stay ON even if the light disappears, it will turn OFF only if its anode current is decreased below IH.	
	iv) Once the LASCR is triggered to ON state, it behaves like a normal SCR.	
	iii) The gate structure is designed to provide sufficient gate sensitivity for triggering from practical sources (e.g. LED).	
	ii) Electron hole pairs created due to radiation produce triggering gate current under the influence of electric field.	

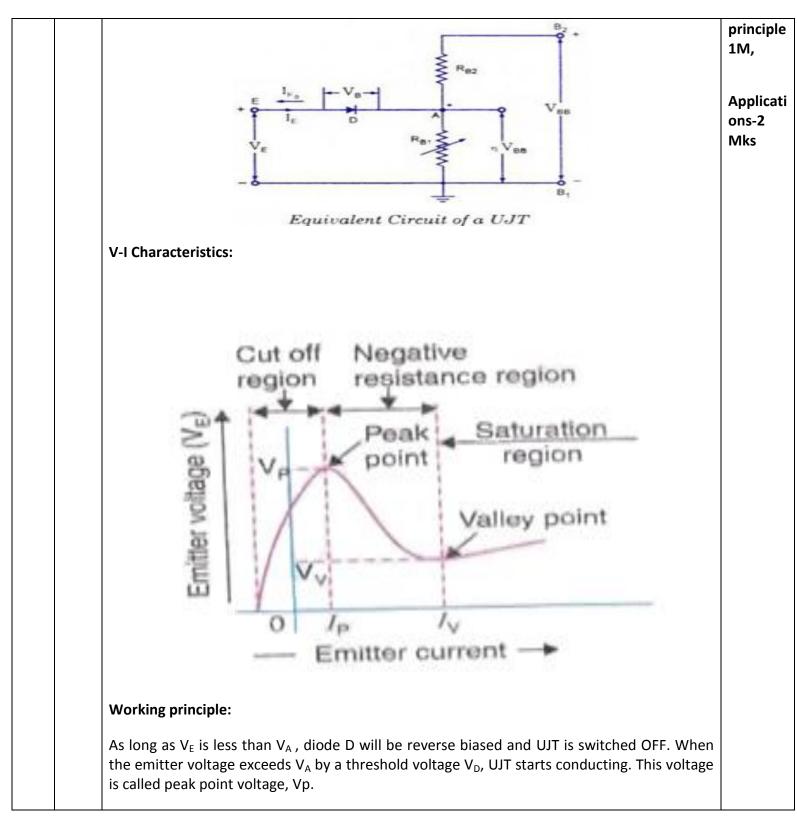
**Subject Name: Basic Power Electronics** 

WINTER-19 EXAMINATION

Subject Code:

22427

## Model Answer



**Subject Name: Basic Power Electronics** 

Subject Code:

22427

$$Vp = V_x + V_D = \eta V_{BB} + V_D$$
 $\eta = \frac{R_{B1}}{R_{BB}}$  $R_{BB} = R_{B1} + R_{B2}$ Once the UJT is ON resistance  $R_{B1}$  reduces rapidly. Decrease in  $R_{B1}$  causes Vx to decrease and  $I_E$  increases.**APPLICATIONS:**1)Triggering device for SCR's and TRIAC's2)Non sinusoidal oscillators3)Saw-tooth generators4)Timing circuits