

15116

17302

| 8 Hours / 100 M | larks | Seat No. | | | | | | | |
|---|--|--|---|---------------------------------------|---------------------------|-------------------|---------------------|--------------------|--------|
| Instructions : | (2) Illust (3) Figur (4) Use perm (5) Mobi | uestions are com rate your answer res to the right in of Non-program issible . le Phone, Pager es are not permi. | rs with nea adicate fu mable El and any o | ll mark lectron other El | ks. vic Poo lectron | cket C nic Con | Calcul | ator i. icatior | s n |
| | | | | | | | | Ν | Iarks |
| 1. a) Attempt any six: | | | | | | | | | 12 |
| i) State types of t | ïlters. | | | | | | | | |
| ii) Draw symbols | of LDR and | Diode. | | | | | | | |
| iii) State types of o | | | | | | | | | |
| iv) Define thermal | - | | | | | | | | |
| | - | OT and AND gate | | • | | | | | |
| | | minals of NPN an | | nsistors | | | | | |
| vii) Define intrinsi | | | | | | | | | |
| viii) Draw VI chara | icteristics of | Pin junction diod | e. | | | | | | 0 |
| b) Attempt any two : | C1 | it a stress of DL C a | | 11.11. | | | | | 8 |
| , | | itecture of PLC a | | II DIOCK | .S. | | | | |
| | - | | | lata anir | fD - | - 25 1- (| _ ת ר | 540 | |
| iii) Sketch circuit | liagrani or no | on-inverting op-ai | np. Calcul | late gan | ιπκ _f - | - 23 K <u>s</u> | 2, κ _i – | - 3 K 52 | • |
| 2. Attempt any four : | | | | | | | | | 16 |
| a) Differentiate Bijun | ction transist | tor and field effect | transistor. | | | | | | |
| b) Draw instrumentat | ion amplifie | r and write its outp | ut voltage | equatio | n. | | | | |
| c) Explain load and li | ne regulation | | | | | | | | |
| d) Illustrate working | of BJT as a sy | witch with diagra | n. | | | | | | |
| e) Draw ladder diag button for one out | | | | oush bu | tton fo | r start | and or | ie pusł | h |
| f) Write truth table a | nd sketch sv | mbol of AND and | | fate | | | | | |

f) Write truth table and sketch symbol of AND and NAND Gate.

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| | | Marks |
|----|--|-------|
| 3. | Attempt any four : | 16 |
| | a) Sketch pin out diagram of IC 741. Lable all pins and state function of each pin. | |
| | b) Differentiate RC, LC and crystal oscillator on the basis of | |
| | i) Component used | |
| | ii) Frequency range | |
| | iii) Frequency stabilityiv) Applications. | |
| | | |
| | c) Sketch circuit diagram, input and output waveform of half wave rectifier. d) What is machatranica? State its any four applications. | |
| | d) What is mechatronics ? State its any four applications. c) Draw since it discourses a draw of act to be marking its and the second state its and the | |
| | e) Draw circuit diagram and waveform of astable multivibrator using IC 555. c) III of the first of the f | |
| | f) Illustrate function of D flip-flop with truth table and logical diagram. | |
| 4. | Attempt any four: | 16 |
| | a) State the principle of R-2R type DAC and write two applications of DAC. | |
| | b) Draw two stage RC coupled amplifier and its frequency response. | |
| | c) What is data logger ? State its applications. | |
| | d) Write features of 8085 microprocessor. | |
| | e) Draw and explain the circuit of op-amp as adder. | |
| | f) Draw block diagram of regulated power supply and write function of each block. | |
| 5. | Attempt any four: | 16 |
| | a) What is transducer ? Write selection criteria for transducer. | |
| | b) State need of signal conditioning. DrawAC signal conditioning system. | |
| | c) Draw single channel data acquisition system and write function of each block. | |
| | d) Draw CB and CE configuration for BJT. | |
| | e) Draw transformer coupled amplifier and its frequency response. | |
| | f) Draw logical diagram of $4:1$ multiplexer and write its truth table. | |
| 6. | Attempt any four: | 16 |
| | a) What is decoder ? Draw logical diagram of 3 : 8 decoder and its truth table. | |
| | b) State the factors on which selection of PLC is based. | |
| | c) Explain the concept of CIM briefly. | |
| | d) What is advance vehicle condition system ? Explain briefly. | |
| | e) State function and applications of robotics. | |
| | f) Draw J-K flip-flop using NAND gate and what is the race around condition ? | |